



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Mary R. Gilliam

Title: ON-CHIP SUBSTRATE REGULATOR TEST MODE

Docket No.: 303.221US3
 Filed: August 22, 2001
 Examiner: Ernest F. Karlsen

Serial No.: 09/935,086
 Due Date: November 26, 2002
 Group Art Unit: 2829

Commissioner for Patents
 Washington, D.C. 20231

We are transmitting herewith the following attached items (as indicated with an "X"):

- ☒ A return postcard.
- ☒ An Amendment and Response (7 Pages).
- ☒ Clean Version of Amended Specification Paragraphs (1 pg.).
- ☒ Clean Version of Pending Claims (7 pgs.).
- ☒ Red-Line Drawing of Figure 1 (1 pg.).


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If an additional fee is required due to changes to the claims, the fee has been calculated as follows:

CLAIMS AS AMENDED						
	(1) Claims Remaining After Amendment		(2) Highest Number Previously Paid For	(3) Present Extra	Rate	Fee
TOTAL CLAIMS	25	-	26		x 18 =	\$0.00
INDEPENDENT CLAIMS	7	-	7		x 84 =	\$0.00
[] MULTIPLE DEPENDENT CLAIMS PRESENTED						\$0.00
TOTAL						\$0.00


Please consider this a PETITION FOR EXTENSION OF TIME for sufficient number of months to enter these papers and please charge any additional required fees or credit overpayment to Deposit Account No. 19-0743.

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By: 
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 Reg. No. 47,857

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Commissioner for Patents, Washington, D.C. 20231, on this 25th day of November, 2002.

Name Amy Moriarty

Signature 

Customer Number 21186

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.
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8/Amend
12.4-02
PATENT CMor

S/N 09/935,086

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Examiner: Ernest F. Karlsen

Serial No.: 09/935,086

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Title: ON-CHIP SUBSTRATE REGULATOR TEST MODE

AMENDMENT AND RESPONSE UNDER 37 CFR § 1.111

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Applicant has reviewed the Office Action mailed on August 26, 2002. Please amend the above-identified patent application as follows.

IN THE DRAWINGS

Enclosed is a copy of Figure 1 of the drawings showing the following proposed amendment to Figure 1 in red ink. A block diagram of a memory device 100 is included with a number of memory cells 110. Applicant notes that page 1, lines 11-12 of the specification as filed provides support for embodiments of the invention incorporated into a memory device. Applicant respectfully submits that one of ordinary skill in the art will appreciate that memory devices include memory cells 110 as included in Figure 1.

Applicant respectfully submits that no new matter has been added as a result of the proposed amendments to Figure 1. Reconsideration and withdrawal of the objections to the drawings is respectfully requested.

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IN THE SPECIFICATION

Please make the paragraph substitutions indicated in the appendix entitled Clean Version of Amended Specification Paragraphs. The specific changes incorporated in the substitute paragraphs are shown in the following marked-up versions of the original paragraphs:

The paragraph beginning on page 2, line 24, is amended as follows:

In one embodiment of Figure 1, a memory device 100 is shown having a number of memory cells 110. The invention is, however not limited to embodiments that include a memory device. Referring to Figure 1, Vcc is a supply voltage level. An n-channel MOSFET MI, has its